

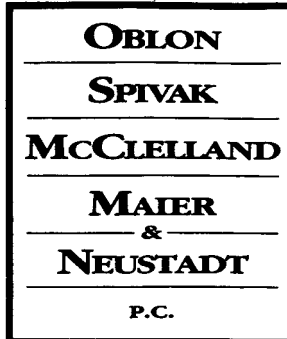


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OCT 30 2001

TC 2800 MAIL ROOM

Docket No.: 195425US0



ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

RE: Application Serial No.:
Applicants: Jianmin QIAO
Filing Date: 09/654,078
For: DUAL DAMASCENE STRUCTURE
AND METHOD OF MAKING
Group Art Unit: 2814
Examiner: PHAM, H.

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SIR:

Attached hereto for filing are the following papers:

RESTRICTION RESPONSE

Our check in the amount of \$ -0- is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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TC OFFICE
MAIL ROOM

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10/31/01
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

:

Jianmin QIAO

: EXAMINER: PHAM, H.

SERIAL NO. : 09/654,078

:

FILED: SEPTEMBER 1, 2000

: GROUP ART UNIT: 2814

FOR: DUAL DAMASCENE STRUCTURE :
AND METHOD OF MAKING

RESTRICTION RESPONSE

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Responsive to the Restriction Requirement of September 28, 2001, Applicants elect,
Group I, Claims 1-10, directed to a semiconductor device, with traverse.

REMARKS

The Examiner has characterized the claim invention as follows:

Group I: Claims 1-10, directed to a semiconductor device;

Group II: Claims 11-17, directed to a method of making a semiconductor device.

Restriction is only proper if the claims of the restricted groups are independent or
patentably distinct and there would be a serious burden placed on the examiner if restriction is